

2. (Amended) The apparatus of claim 1, wherein the first, second, and third circuit branches include:

first, second, and third resistances, respectively, each having one terminal coupled to a first voltage;

first, second, and third current mirror devices, respectively, each having one terminal coupled to an opposite terminal of the first, second, and third resistances, respectively, the third current mirror device matching the first current mirror device, the first current mirror device coupled to the second current mirror device in a current mirror configuration; and

first, second, and third loads, respectively, each having one terminal coupled to an opposite terminal of the first, second, and third current mirror devices, respectively, a second terminal coupled to a second voltage, the third load matching the first load, and a third terminal coupled to the enable node.

3. (Amended) The apparatus of claim 2, further comprising a second branch output node coupled to the second branch and the first branch current mirror, a second branch output node potential to decrease if the second resistance increases relative to the first resistance and to increase if the second resistance decreases relative to the first resistance, the amplifier output node potential being equivalent to the second branch output node potential if the second resistance is equivalent to the first resistance.

4. (Amended) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance.

5. (Amended) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance and to trip if the first resistance is approximately equal to the second resistance.

6. (Amended) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance and to trip the amplifier if the potential on the first current mirror device terminal coupled to the first resistance is approximately equal to the second branch output node potential.
7. (Amended) The apparatus of claim 3, wherein an amplifier output node potential is equivalent to the second branch output node potential if the first resistance is approximately equal to the second resistance.
8. The apparatus of claim 2, the first, second, and third resistances each further comprising a fuse element.
9. The apparatus of claim 1, the first, second, and third current mirror devices each further comprising p-channel material.
10. (Canceled) A resistance sense circuit, comprising:
first circuit branch;
second circuit branch coupled to the first branch in a current mirror configuration;
an amplifier coupled to the second circuit branch, the amplifier comprising a load having its gate coupled to a first voltage and its source coupled to a second voltage, and an output node; and
an enable node coupled to the first circuit branch and the second circuit branch to turn the current mirror on to enable the resistance sense circuit to indicate a logical state at the output node if an enable signal is asserted on the enable node, the first circuit branch is unprogrammed and the second circuit branch is programmed.
11. (Canceled) The resistance sense circuit of claim 10, the output node further to indicate a value approximately equal to the first voltage if the enable signal is asserted and to indicate a value approximately equal to the second voltage if the enable signal is de-asserted.

12. (Canceled) The resistance sense circuit of claim 10, the first resistance further comprising a resistor and the second resistance further comprising a fuse element.
13. (Canceled) The resistance sense circuit of claim 10, further comprising:
first input node coupled to the second circuit branch to program the second circuit branch with a first logical state; and
second input node coupled to the first circuit branch to program the first circuit branch with a second logical state.
14. (Canceled) The resistance sense circuit of claim 10, further comprising:
first resistance and second resistance each having one terminal coupled to a voltage;
first and second current mirror device each having one terminal coupled to an opposite terminal of the first and second resistance, respectively, the first current mirror device coupled to the second current mirror device in a current mirror configuration;
third current mirror device matching the first current mirror device, the third current mirror device having a gate coupled to a second circuit branch output node and a source coupled to the voltage; and
first and second load each having one terminal coupled to an opposite terminal of the first and second current mirror device, respectively.
15. (Canceled) The resistance sense circuit of claim 14, further comprising:
first input node coupled between the second current mirror device and the second resistance to program the second resistance with a first logical state; and
second input node coupled between the first current mirror device and the first resistance to program the first resistance with a second logical state.
16. (Canceled) An apparatus, comprising:
a sense amplifier;
a current mirror having a current mirror output node and a current mirror drain; and
a differential amplifier coupled to the sense amplifier via the current mirror output node, the differential amplifier to change states if a potential on the current mirror output node is approximately equal to a potential on the current mirror drain.

17. (Canceled) The apparatus of claim 16, further comprising first resistance and second resistance coupled to a current mirror source.

18. (Canceled) The apparatus of claim 16, further comprising first resistance and second resistance coupled to a current mirror source, the first and second resistances comprising p-channel material.

19. (Canceled) The apparatus of claim 16, further comprising:
first resistance coupled to a current mirror source;
second resistance coupled to the current mirror source; and
an enable node coupled to the current mirror drain to enable the differential amplifier to indicate a logical state at a differential amplifier output node if an enable signal is asserted on the enable node, the first circuit resistance is un-programmed and the second resistance is programmed.

20. (Canceled) The apparatus of claim 16, the differential amplifier further comprising a second current mirror output node coupled to an inverting input of the differential amplifier.

21. An apparatus, comprising:
an amplifier having an output node; and
a gain stage, coupled to the amplifier, having a trip point to track a potential on the output node.

22. The apparatus of claim 21, the amplifier comprising a sense branch coupled to a reference branch in a current mirror configuration.

23. The apparatus of claim 22, the gain stage comprising a scaled replica of the reference branch or the sense branch.

24. The apparatus of claim 22, further comprising first and second voltages each coupled to the amplifier and the gain stage.